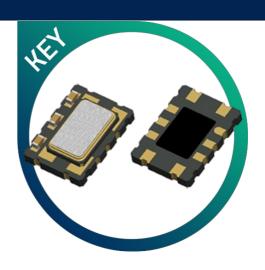
GTXO-C76V

CMOS VCTCXO High Precision High

Temperature Tristate

- High Precision ±0.05ppm
- High Temperature Operation +105°C
- CMOS output
- Tri-state function



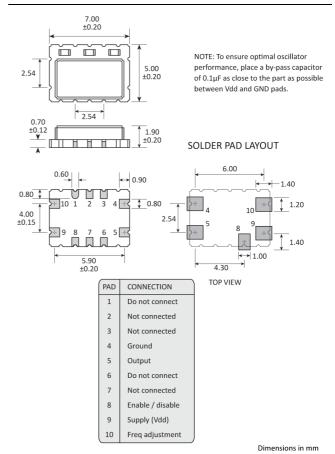
CONFIGURABLE OPTIONS	
Parameter	Option Code
Frequency	
Frequency stability	
Any	
±0.05ppm	А
±0.1ppm	В
±0.2ppm	Р
±0.28ppm	D
±0.5ppm	E
±1.0ppm	F
Temperature range	
Any	
-20 to +70°C	N
-40 to +85°C	I
-40 to +105°C	D
Supply voltage (V _{DD})	
Any	
+3.3V ±5%	L
+2.5V ±5%	J

^{*}Some combinations of frequency stability and wide temperature ranges may not be available. Our team will advise accordingly if this affects your enquiry.

SPECIFICATIONS

Dimensions 7.0 x 5.0 x 2.10 Storage -55 to +125°C temperature range Supply voltage stability Load stability Load stability Ageing ±1.0ppm max first year Supply current 7.5mA max Driving ability 15pF CMOS Waveform 45:55 @ 50%VDD symmetry Logic levels '0' level = 10%VDD max '1' level = 90%VDD min Start up time 5ms max Frequency adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow Tolerance @25°C		
Storage temperature range Supply voltage stability Load stability Load stability Ageing ±1.0ppm max first year Supply current 7.5mA max Driving ability 15pF CMOS Waveform 45:55 @ 50%VDD symmetry Logic levels '0' level = 10%VDD max '1' level = 90%VDD min Start up time 5ms max Frequency adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 10kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Frequency range	10.0 ~ 52.0MHz
temperature range Supply voltage	Dimensions	7.0 x 5.0 x 2.10
Supply voltage stability Load stability Load stability ±0.05ppm, Z _L ±10% Ageing ±1.0ppm max first year Supply current 7.5mA max Driving ability 15pF CMOS Waveform 45:55 @ 50%V _{DD} symmetry Logic levels '0' level = 10%V _{DD} max '1' level = 90%V _{DD} min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Storage	-55 to +125°C
stability Load stability Ageing ±1.0ppm max first year Supply current 7.5mA max Driving ability 15pF CMOS Waveform 45:55 @ 50%VDD symmetry Logic levels '0' level = 10%VDD max '1' level = 90%VDD min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 10kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	temperature range	
Load stability ±0.05ppm, Z _L ±10% Ageing ±1.0ppm max first year Supply current 7.5mA max Driving ability 15pF CMOS Waveform 45:55 @ 50%V _{DD} symmetry Logic levels '0' level = 10%V _{DD} max	Supply voltage	±0.1ppm, V _{DD} ±5%
Ageing ±1.0ppm max first year Supply current 7.5mA max Driving ability 15pF CMOS Waveform 45:55 @ 50%VpD symmetry '0' level = 10%VpD max '1' level = 90%VpD min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment -130dBc/Hz @ 100Hz Phase noise (typ @ -148dBc/Hz @ 1kHz - 156dBc/Hz @ 1kHz - 156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	stability	
Supply current 7.5mA max Driving ability 15pF CMOS Waveform 45:55 @ 50%VDD symmetry '0' level = 10%VDD max '1' level = 90%VDD min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 10kHz -156dBc/Hz @ 10kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Load stability	±0.05ppm, Z _L ±10%
Driving ability 15pF CMOS Waveform 45:55 @ 50%V _{DD} symmetry '0' level = 10%V _{DD} max Logic levels '0' level = 90%V _{DD} min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment -130dBc/Hz @ 100Hz Phase noise (typ @ 20.0MHz) -148dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Ageing	±1.0ppm max first year
Waveform 45:55 @ 50%VDD symmetry '0' level = 10%VDD max Logic levels '0' level = 90%VDD min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Supply current	7.5mA max
symmetry Logic levels '0' level = 10%V _{DD} max '1' level = 90%V _{DD} min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Driving ability	15pF CMOS
Logic levels '0' level = 10%V _{DD} max '1' level = 90%V _{DD} min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Waveform	45:55 @ 50%V _{DD}
'1' level = 90%V _{DD} min Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	symmetry	
Start up time 5ms max Frequency ±5ppm min, +1.5V ±1.0V adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Logic levels	'0' level = 10%V _{DD} max
#5ppm min, +1.5V ±1.0V adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow		'1' level = 90%V _{DD} min
adjustment Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Start up time	5ms max
Phase noise (typ @ -130dBc/Hz @ 100Hz 20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Frequency	±5ppm min, +1.5V ±1.0V
20.0MHz) -148dBc/Hz @ 1kHz -156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	adjustment	
-156dBc/Hz @ 10kHz Frequency ±1.5ppm max, 60 mins after reflow	Phase noise (typ @	-130dBc/Hz @ 100Hz
Frequency ±1.5ppm max, 60 mins after reflow	20.0MHz)	-148dBc/Hz @ 1kHz
		-156dBc/Hz @ 10kHz
Tolerance @25°C	Frequency	±1.5ppm max, 60 mins after reflow
	Tolerance @25°C	

PACKAGE DRAWING



ORDERING INFORMATION

To request a quotation for the GTXO-C76V please use the configurable options form to choose the options you require and then submit your configured product to our team. Our expert advisers are always happy to help with your requirements and can be contacted on +44 1460 256 100 or at sales@golledge.com.

Once we've received your request our expert team will then produce a quotation tailored to meet your needs using the option codes you've selected.

Following product selection you will be issued with a seven character Golledge part number. Your Golledge part number is the internationally accepted Golledge manufacturing part number (MPN) that should be used for all project documentation, including bills of materials (BoMs) and purchase orders.

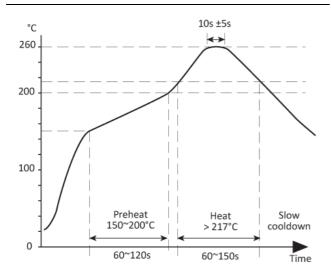
If you have any queries regarding any of our documentation our dedicated sales team will be happy to help.

MARKING

FREQUENCY
DC

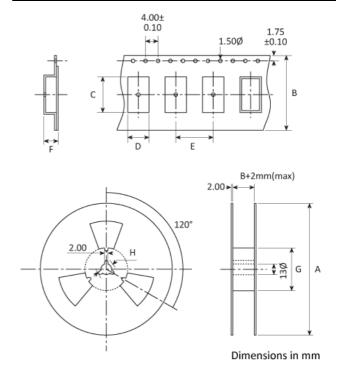
 Pin 1 Marking type: Laser DC = Date code

SOLDERING PROFILE



Lead free solderability limits: 260°C ±5°C x 10s ±5s x 2.

TAPE & REEL SPECIFICATION



HANDLING & STORAGE



Human Body Model (HBM) 1A (250V to <500V)



Moisture Sensitivity Level (MSL): 1 (or not applicable)

COMPLIANCE



(P6) Lead-free (< 0.1% by weight)



RoHS compliant with no exemptions.

declaration



REACH compliant.



Au Ta Sn W Free of conflict minerals. See our declaration



Free of Halogens. See our declaration



Free of Ozone-depleting substances. See our

declaration