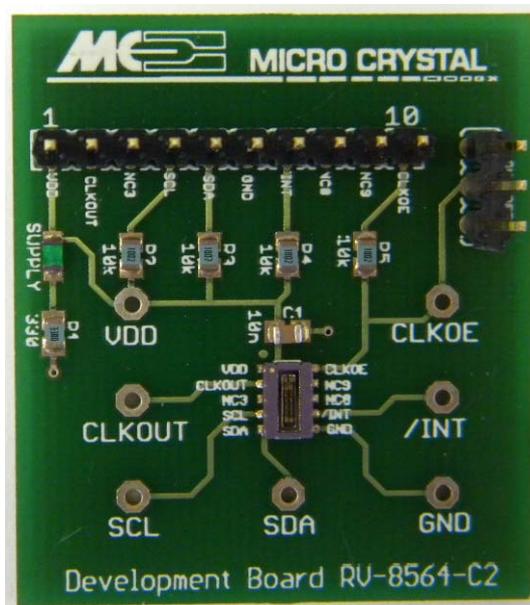


# DEVELOPMENT BOARD



# RV-8564-C2

Low-Power Real Time Clock / Calendar Module

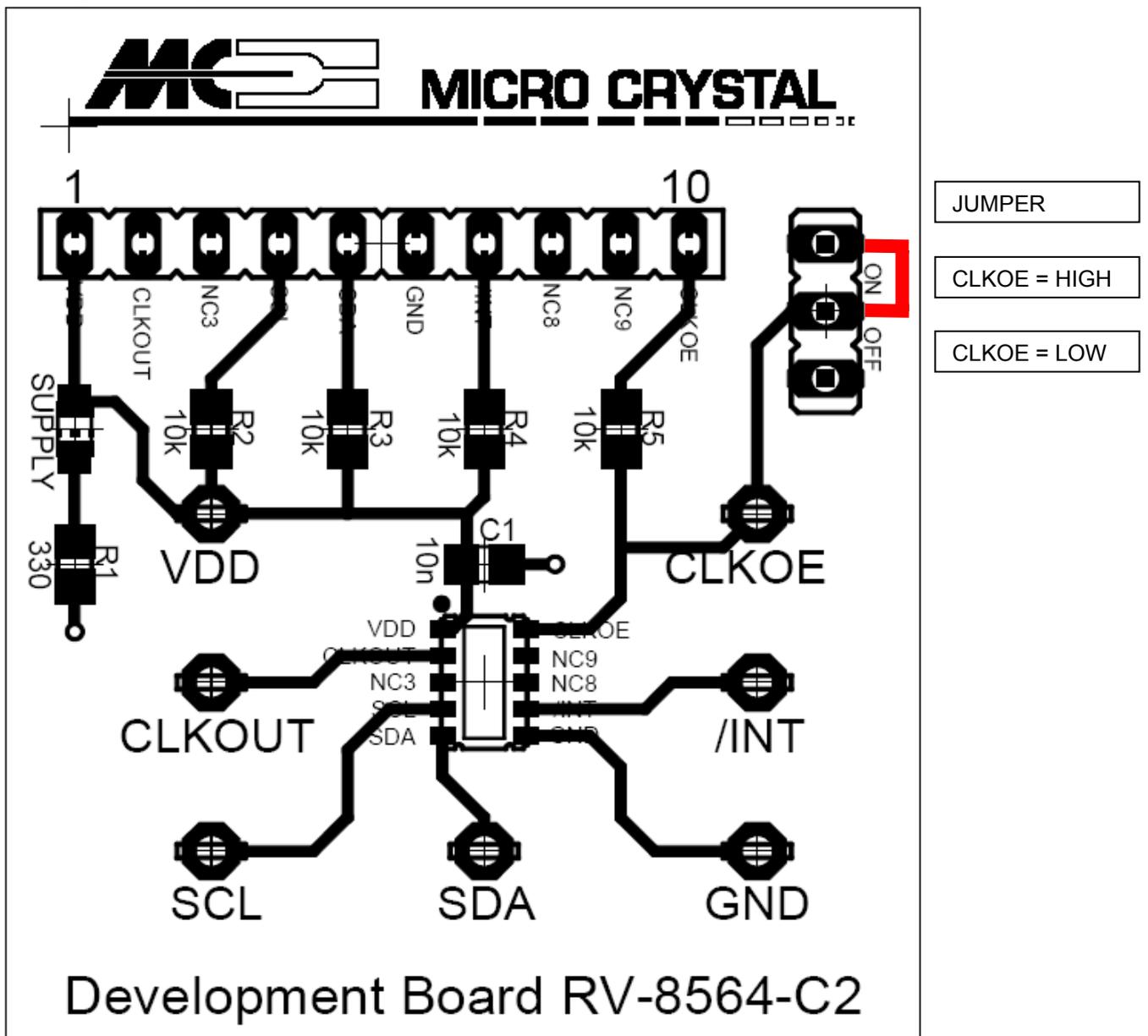
### RV-8564-C2

The RV-8564-C2 is soldered onto the Development Board.  
 Every pin is either accessible at test pins 1 – 10 or at the test vias situated around the device.

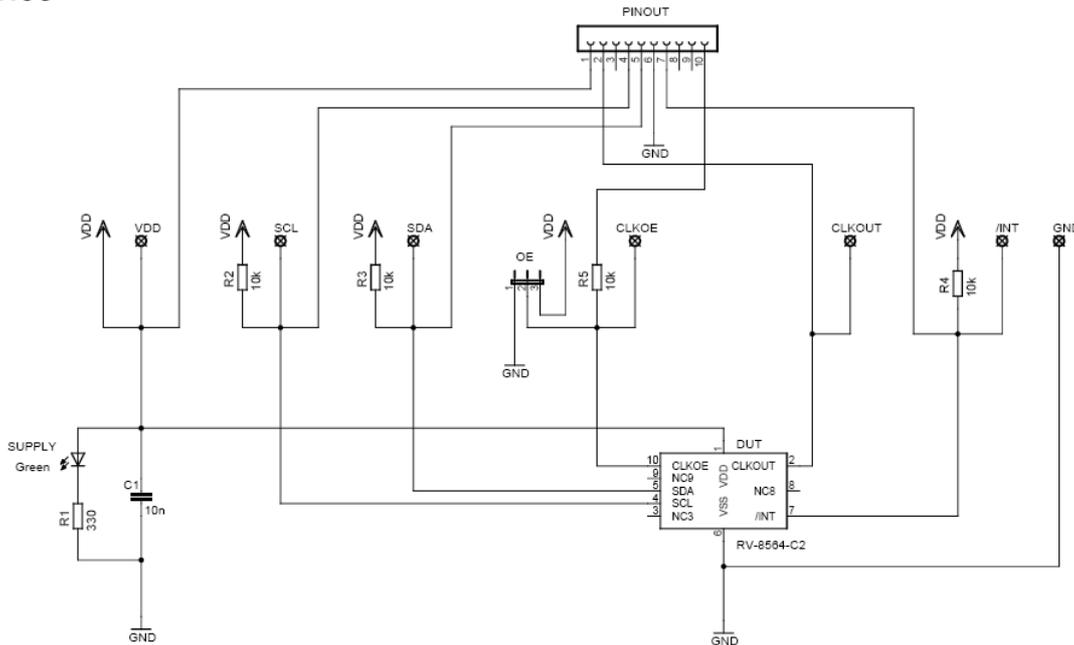
The following passive components are already soldered on the Board:

C1	10 nF	Decoupling capacitor between $V_{SS}$ and $V_{DD}$
R1	330 $\Omega$	current limiting resistor for LED
LED	green	Supply, current consumption of the LED has to be considered
R2	10 k $\Omega$	Pull-up resistor SCL to $V_{DD}$
R3	10 k $\Omega$	Pull-up resistor SDA to $V_{DD}$
R4	10 k $\Omega$	Pull-up resistor INT to $V_{DD}$
R5	10 k $\Omega$	Protection resistor to prevent short-circuit between external CLKOE signal and Jumper.

#### DEVELOPMENT BOARD



**SCHEMATICS**



**PINOUT RV-8564-C2**

	# 1	$V_{DD}$	# 10	CLKOE
	# 2	CLKOUT	# 9	N.C.
	# 3	N.C	# 8	N.C.
	# 4	SCL	# 7	INT
	# 5	SDA	# 6	$V_{SS}$

**PIN DESCRIPTION**

Symbol	Pin #	Description
$V_{DD}$	1	Positive supply voltage; recommended 10 nF decoupling capacitor close to device
CLKOUT	2	Clock Output pin; push-pull output; at power-up by default 32.768kHz
NC	3	Not Connected
SCL	4	Serial Clock Input pin; requires pull-up resistor
SDA	5	Serial Data Input-Output pin; open-drain; requires pull-up resistor.
$V_{SS}$	6	Ground
INT	7	Interrupt Output pin; open-drain; active LOW
NC	8	Not Connected
NC	9	Not Connected
CLKOE	10	CLKOUT enable/disable pin; enable is active HIGH

Datasheet and Application-Manual are available for download under: [www.microcrystal.com](http://www.microcrystal.com)