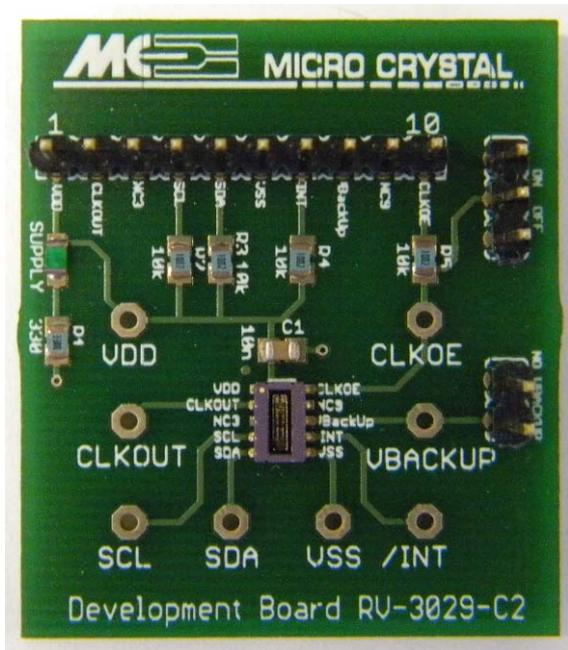


# DEVELOPMENT BOARD



# RV-3029-C2

Temperature Compensated Real Time Clock / Calendar Module

### RV-3029-C2

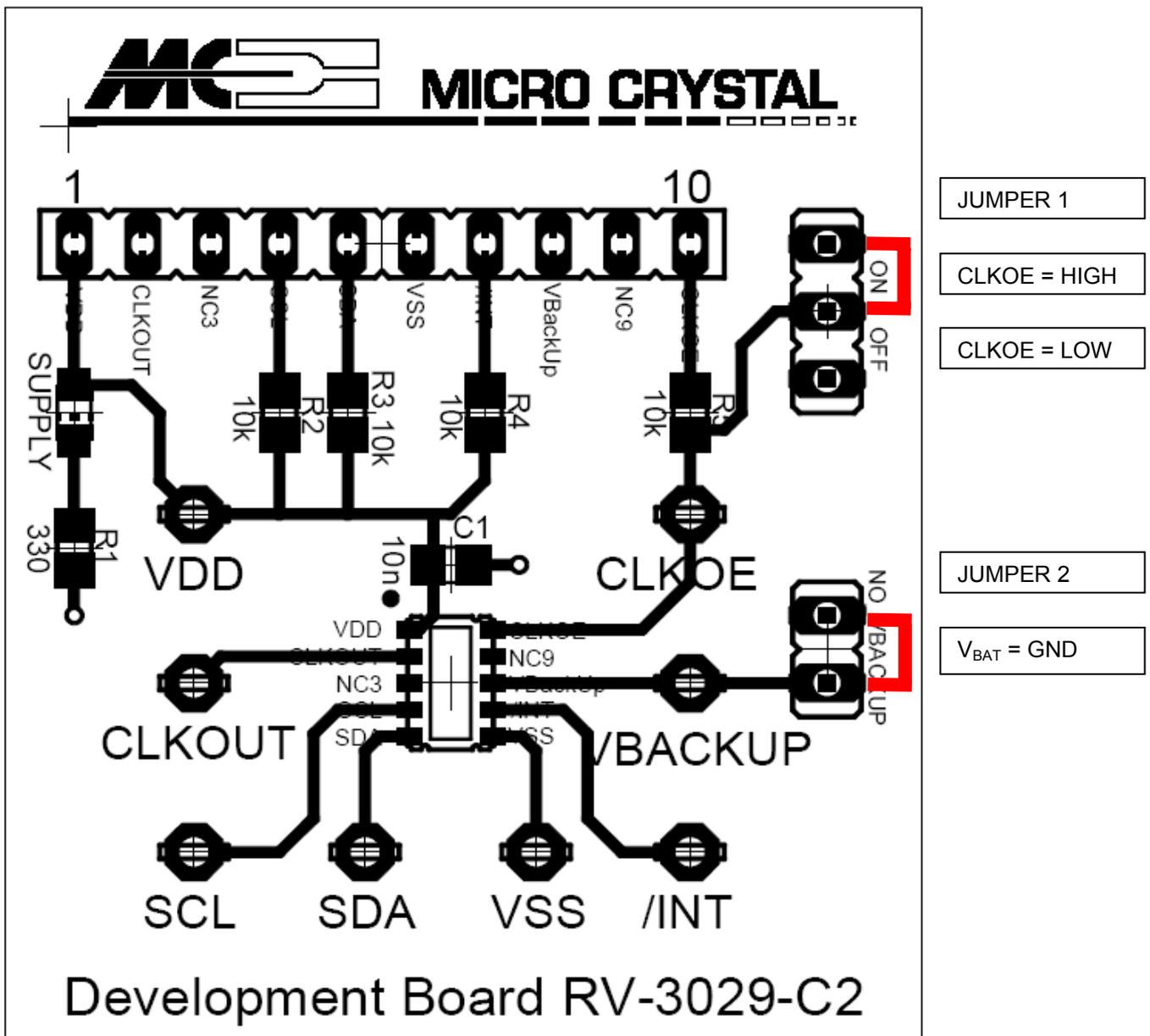
The RV-3029-C2 is soldered onto the Development Board.

Every pin is either accessible at test pins 1 – 10 or at the test vias situated around the device.

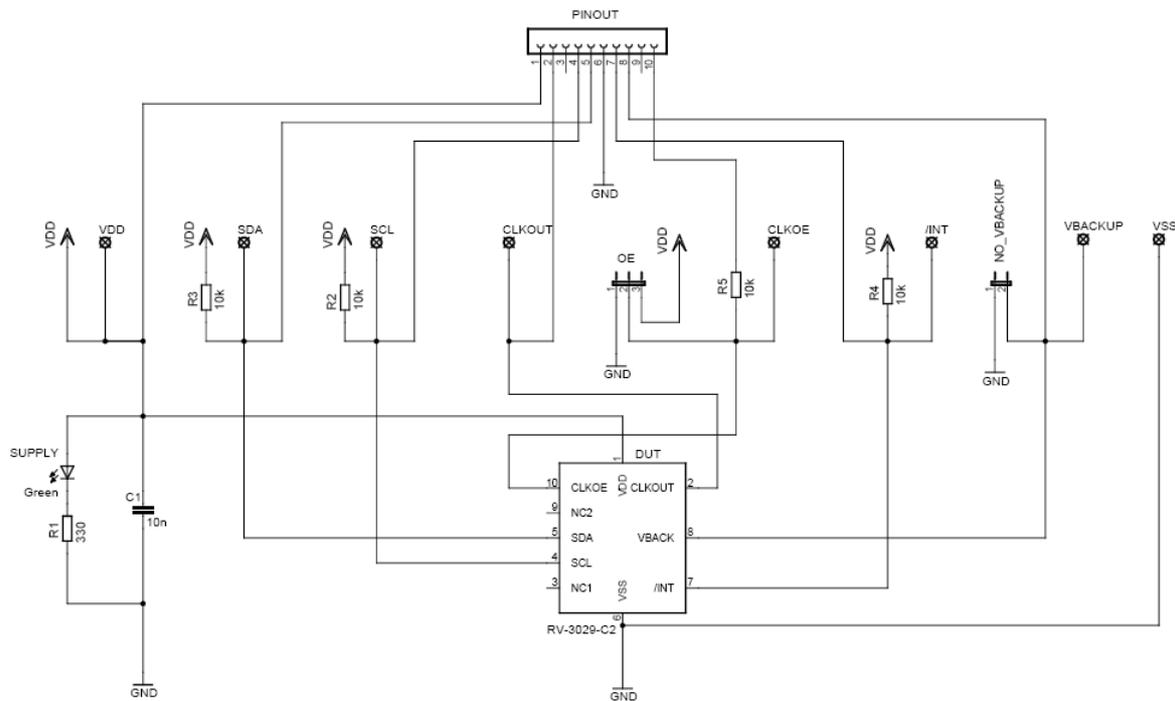
The following passive components are already soldered on the Board:

- |     |               |  |
|-----|---------------|--|
| C1  | 10 nF         | Decoupling capacitor between $V_{SS}$ and $V_{DD}$                                     |
| R1  | 330 $\Omega$  | current limiting resistor for LED  |
| LED | green         | Supply, current consumption of the LED has to be considered                            |
| R2  | 10 k $\Omega$ | Pull-up resistor SCL to $V_{DD}$   |
| R3  | 10 k $\Omega$ | Pull-up resistor SDA to $V_{DD}$   |
| R4  | 10 k $\Omega$ | Pull-up resistor INT to $V_{DD}$   |
| R5  | 10 k $\Omega$ | Protection resistor to prevent short-circuit between external CLKOE signal and Jumper. |

#### DEVELOPMENT BOARD



SCHEMATICS



PINOUT RV-3029-C2

# 1	V <sub>DD</sub>	# 10	CLKOE
# 2	CLKOUT	# 9	N.C.
# 3	SCL	# 8	$\overline{\text{INT}}$
# 4	SDI	# 7	CE
# 5	SDO	# 6	V <sub>SS</sub>

PIN DESCRIPTION

Symbol	Pin #	Description
V <sub>DD</sub>	1	Positive supply voltage; positive or negative steps in supply voltage may affect oscillator performance, recommend 10 nF decoupling capacitor close to device
CLKOUT	2	Clock Output pin. CLKOUT or INT function can be selected.(Control_1; bit7; CLK/INT) CLKOUT output push-pull / INT function open-drain requiring pull-up resistor
NC	3	Not Connected; internally used for test, do not connect other signals then ground.
SCL	4	Serial Clock Input pin; requires pull-up resistor
SDA	5	Serial Data Input-Output pin; open-drain; requires pull-up resistor
V <sub>SS</sub>	6	Ground
$\overline{\text{INT}}$	7	Interrupt Output pin; open-drain; active LOW
V <sub>BACKUP</sub>	8	Backup Supply Voltage; tie to GND when not using backup supply voltage.
NC	9	Not Connected; internally used for test, do not connect other signals then ground.
CLKOE	10	CLKOUT enable/disable pin; enable is active HIGH; tie to GND when not using CLKOUT