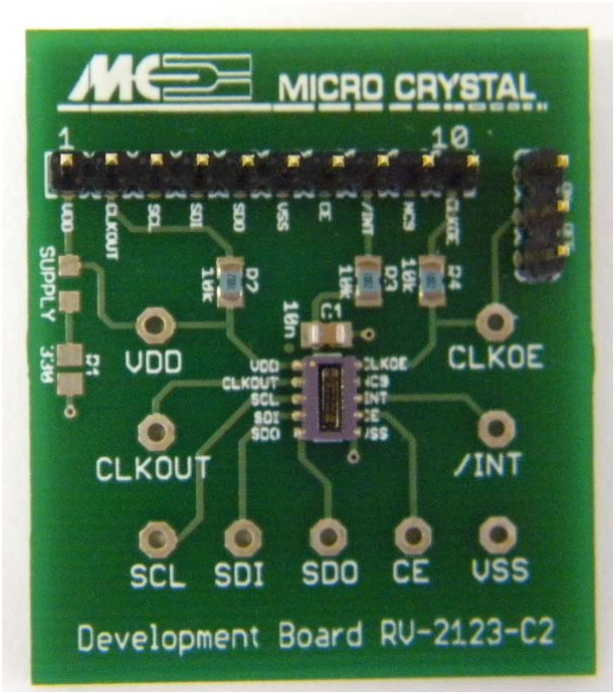
MICRO CRYSTAL SWITZERLAND

RV-2123-C2
Development Board

DEVELOPMENT BOARD



RV-2123-C2

Ultra-Low-Power Real Time Clock / Calendar Module

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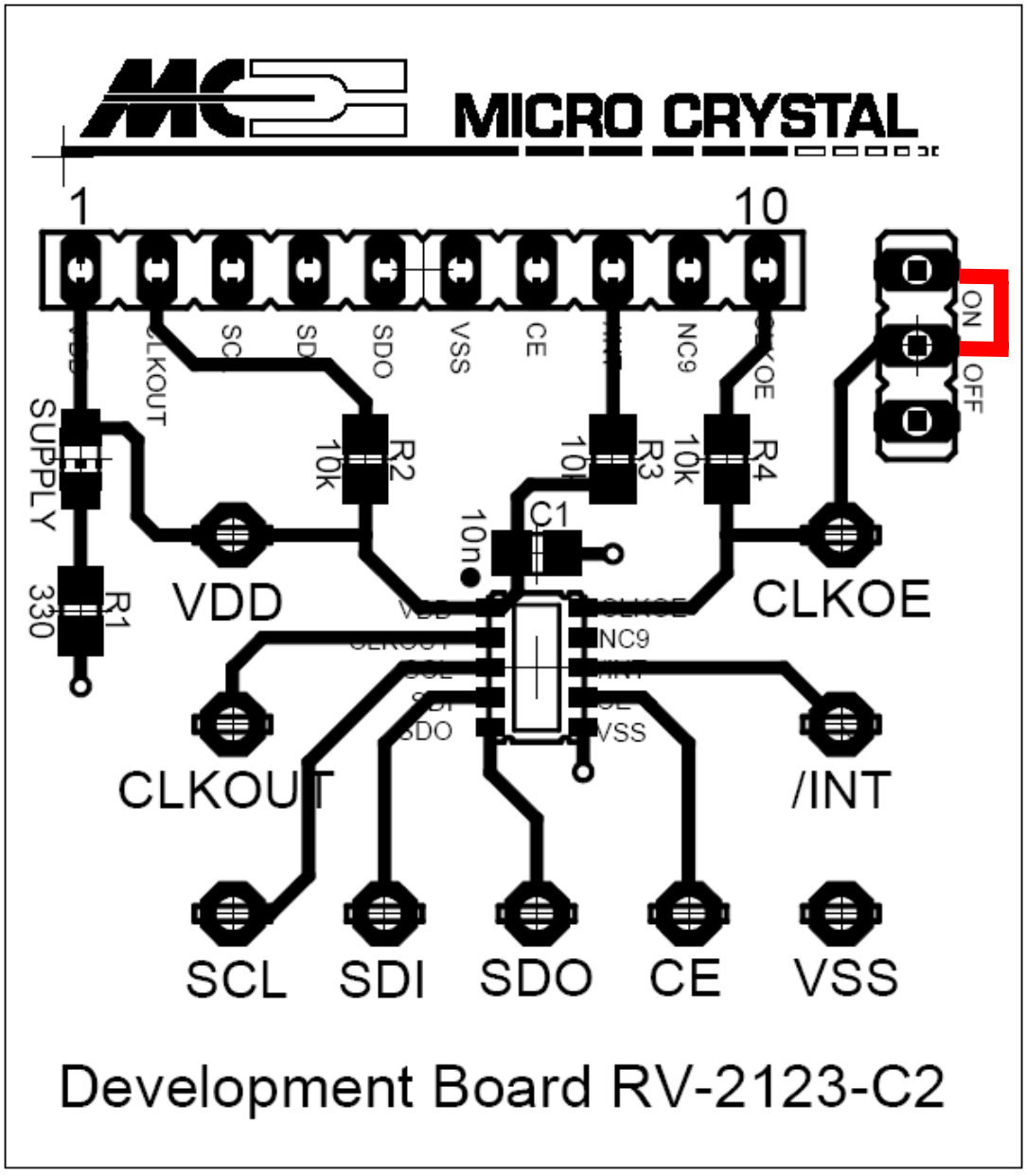
RV-2123-C2

The RV-2123-C2 is soldered onto the Development Board.
Every pin is either accessible at test pins 1 – 10 or at the test vias situated around the device.

The following passive components are already soldered on the Board:

C1	10 nF	Decoupling capacitor between V_{SS} and V_{DD}
R1	not populated	
LED	not populated, to allow measuring the current-consumption of the device	
R2	10 kΩ	Pull-up resistor CLKOUT to V_{DD}
R3	10 kΩ	Pull-up resistor INT to V_{DD}
R4	10 kΩ	Protection resistor to prevent short-circuit between external CLKOE signal and Jumper.

DEVELOPMENT BOARD

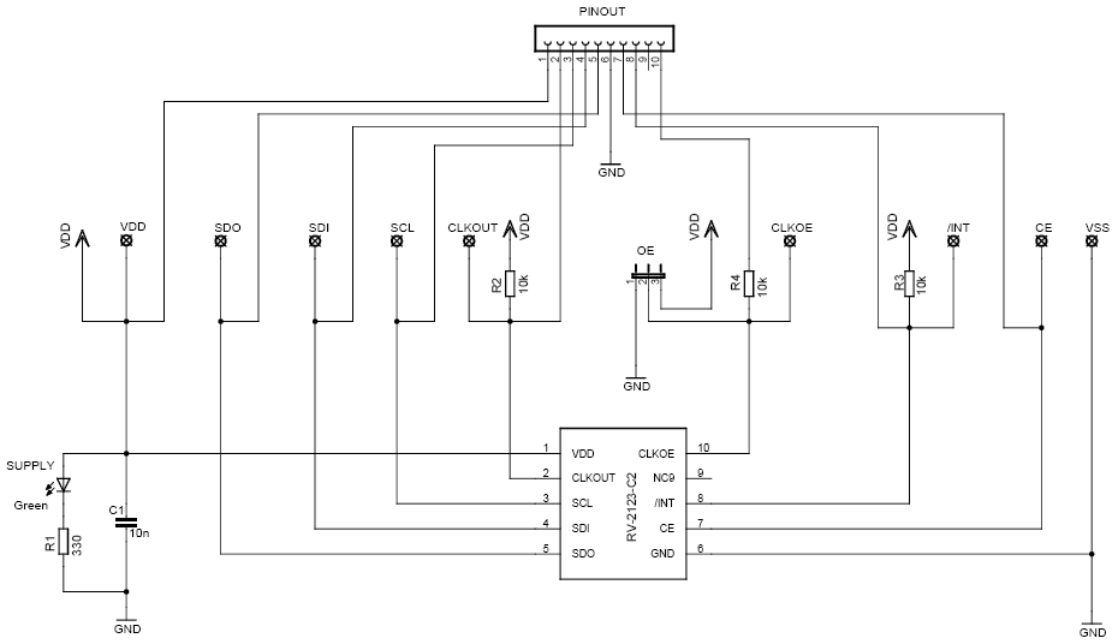


JUMPER

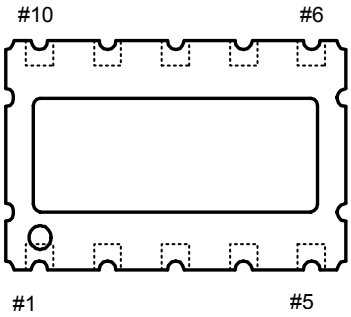
CLKOE = HIGH

CLKOE = LOW

SCHEMATICS



PINOUT RV-2123-C2



# 1	V_{DD}	# 10	CLKOE
# 2	CLKOUT	# 9	N.C.
# 3	SCL	# 8	$\overline{INT}$
# 4	SDI	# 7	CE
# 5	SDO	# 6	V_{SS}

PIN DESCRIPTION

Symbol	Pin #	Description
V_{DD}	1	Positive supply voltage; positive or negative steps in supply voltage may affect oscillator performance, recommend 10 nF decoupling capacitor close to device
CLKOUT	2	Clock Output pin; open-drain
SCL	3	Serial Clock Input pin; may float when CE inactive
SDI	4	Serial Data Input pin; may float when CE inactive
SDO	5	Serial Data Output pin; push-pull; high-impedance when not driving; can be connected to SDI for single-wire data line
V_{SS}	6	Ground
CE	7	Chip Enable input; active HIGH; with internal pull-down
$\overline{INT}$	8	Interrupt output pin; open-drain; active LOW
NC	9	Not Connected
CLKOE	10	CLKOUT enable/disable pin; enable is active HIGH

Datasheet and Application-Manual are available for download under: www.microcrystal.com