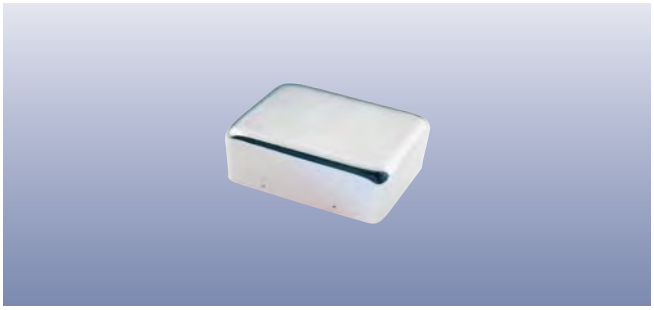
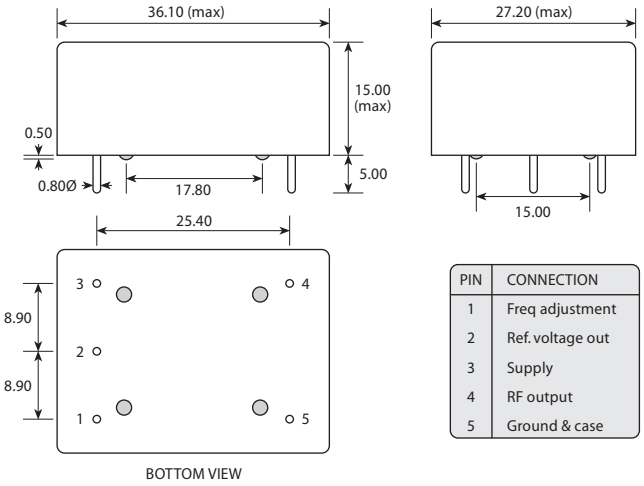
OCXO CMOS Output VHF

HCD381





BOTTOM VIEW

PIN	CONNECTION
1	Freq adjustment
2	Ref. voltage out
3	Supply
4	RF output
5	Ground & case

Features

- **-160dBc/Hz noise floor**
- **Frequencies up to 100MHz**
- **No multiplier**
- **Standard European IEC CO-08 pin-out**
- **Custom options available**

Specifications

Parameters	Product HCD381	Option Codes
Frequency range: 30.0 ~ 100MHz	■	
Ageing per day (at despatch):		
< $5x10^{-9}$	□	B
< $3x10^{-9}$	□	C
< $1x10^{-9}$	■	D
Frequency stability:		
< 1 to $5x10^{-7}$ per year	■	
< $5x10^{-9}$ per 10% change in V_{DD}	■	
Temperature stability: < $5x10^{-8}$	□	N
< $2x10^{-8}$	□	P
< $1x10^{-8}$	■	R
Operating temperature range:		
0 to +50°C	□	A
-10 to +60°C	■	C
-20 to +70°C	□	F
Storage temperature range:		
-40 to +90°C	■	
Output waveform:		
CMOS / TTL compatible	■	
Frequency adjustment:		
±$5x10^{-7}$ (typ) over +0.5 to +6.0V (sufficient for 10 years ageing min) Stabilised +6.0V supply provided	■	
Supply voltage (V_{DD}): +9V (±0.5V)	□	M
+12V (±0.5V)	■	N
+15V (±0.5V)	□	P
+18V (±0.5V)	□	R
Power consumption:		
4.5W max at switch on	■	
1.0W typ when stabilised at +25°C	■	
Warm up:		
< $5x10^{-8}$ after 10mins at +25°C	■	
Allan deviation (ADEV), 1 sec:		
< $2x10^{-11}$	■	
Phase noise (@ 50.0MHz):		
< -75 dBc/Hz @ 1Hz	■	
< -105 dBc/Hz @ 10Hz	■	
< -135 dBc/Hz @ 100Hz	■	
< -150 dBc/Hz @ 1kHz	■	
< -160 dBc/Hz @ 10kHz	■	
< -160 dBc/Hz @ 50kHz	■	
Shock: IEC 68-2-27 Test Ea 50G for 11ms	■	
Vibration: IEC 68-2-06 Test Fc 10-55Hz, 1.5mm. 55-500Hz, 10G	■	

■ Standard. □ Optional - Please specify required code(s) when ordering

Ordering Information

Part No, or product name + option codes + frequency
eg: **HCD381/BPCN 100MHz**
HCD381/CRCN 50.0MHz
Option code X (eg HCD381/X) denotes a custom specification.

- Some combinations of frequency and specification may not be available, or may be subject to extended leadtimes

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